

Y65KPJ

PHASE CONTROL THYRISTOR

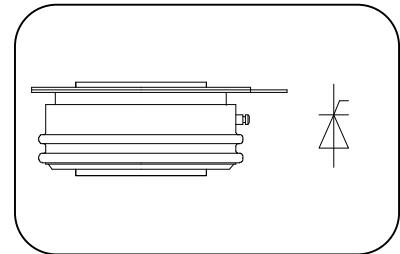
Features:

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

Typical Applications

- AC controllers
- DC and AC motor control
- Controlled rectifiers

$I_{T(AV)}$ 1611A
 V_{DRM}/V_{RRM} 3100~4200V
 I_{TSM} 19.3 KA
 I^2t 1862 $10^3 A^2 s$



SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T_f (°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled, $T_{hs}=55^\circ C$	125			1611	A
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	$V_{DRM} \& V_{RRM}$ tp=10ms $V_{DSM} \& V_{RSM} = V_{DRM} \& V_{RRM} + 100V$	125	3100		4200	V
I_{DRM} I_{RRM}	Repetitive peak current	$V_{DM} = V_{DRM}$ $V_{RM} = V_{RRM}$	125			120	mA
I_{TSM}	Surge on-state current	10ms half sine wave	125			19.3	KA
I^2t	I^2T for fusing coordination	$V_R=0.6V_{RRM}$				1862	$A^2 s * 10^3$
V_{TO}	Threshold voltage		125			1.17	V
r_T	On-state slop resistance					0.35	mW
V_{TM}	Peak on-state voltage	$I_{TM}=3000A, F=31KN$	125			2.22	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$	125			800	V/μs
di/dt	Critical rate of rise of on-state current	$V_{DM}=67\%V_{DRM}$ to 2000A, Gate source 1.5A $t_r \leq 0.5 \mu s$ Repetitive	125			150	A/μs
I_{rm}	Reverse recovery current		125			250	A
t_{rr}	Reverse recovery time	$I_{TM}=1500A, tp=1000\mu s, di/dt=-20A/\mu s, V_R=50V$				21	μs
Q_{rr}	Recovery charge					2100	μC
I_{GT}	Gate trigger current		25	40		300	mA
V_{GT}	Gate trigger voltage	$V_A=12V, I_A=1A$		0.8		3.0	V
I_H	Holding current			20		300	mA
V_{GD}	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$	125	0.3			V
$R_{th(j-h)}$	Thermal resistance Junction to heat sink	At 180° sine double side cooled Clamping force 32KN				0.017	°C /W
F_m	Mounting force			27		34	KN
T_{stg}	Stored temperature			-40		140	°C
W_t	Weight					650	g
Outline				KT60cT65			

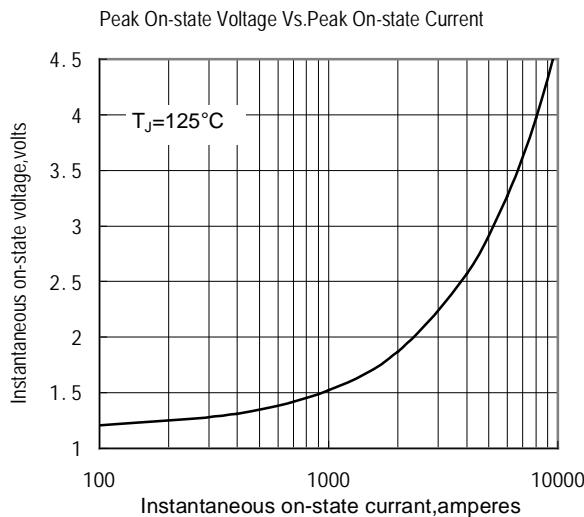


Fig.1

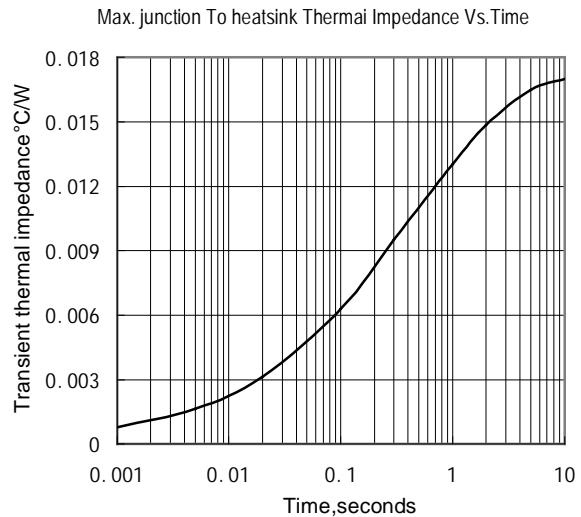


Fig.2

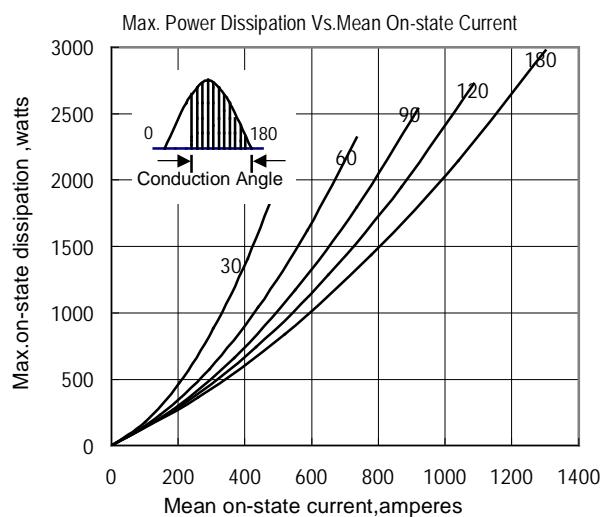


Fig.3

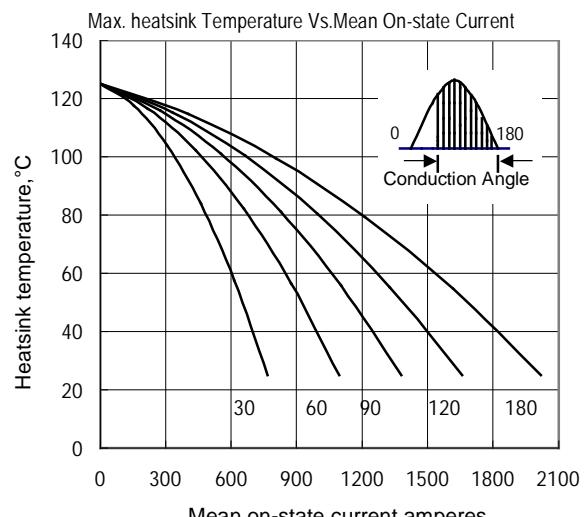


Fig.4

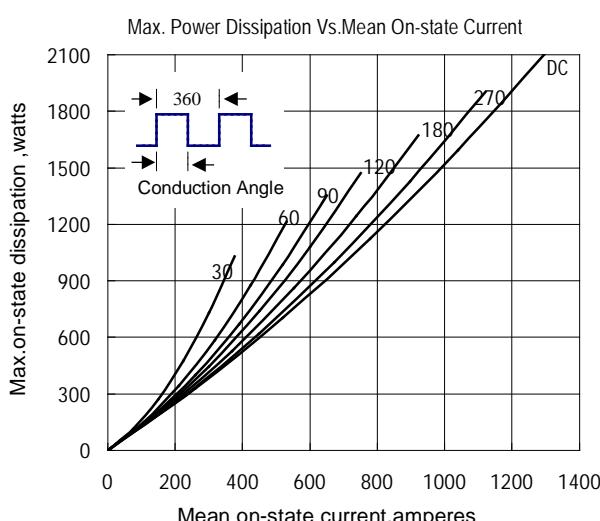


Fig.5

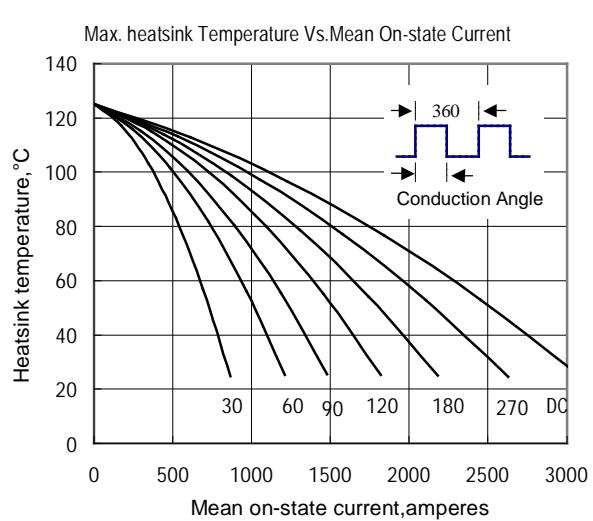


Fig.6

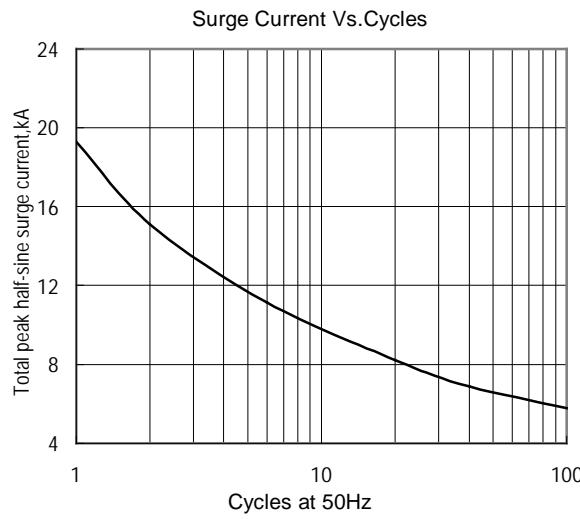


Fig.7

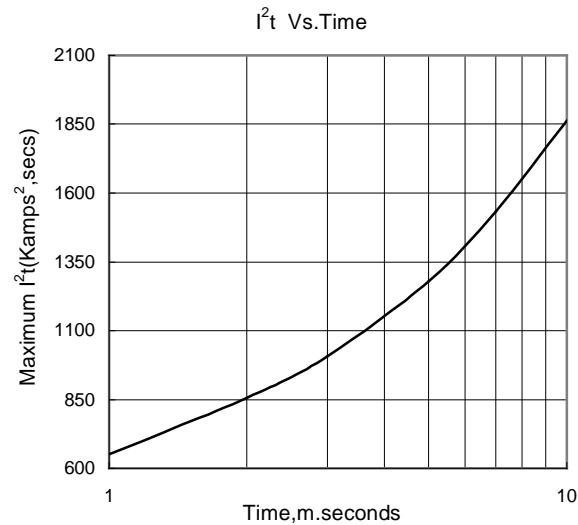


Fig.8

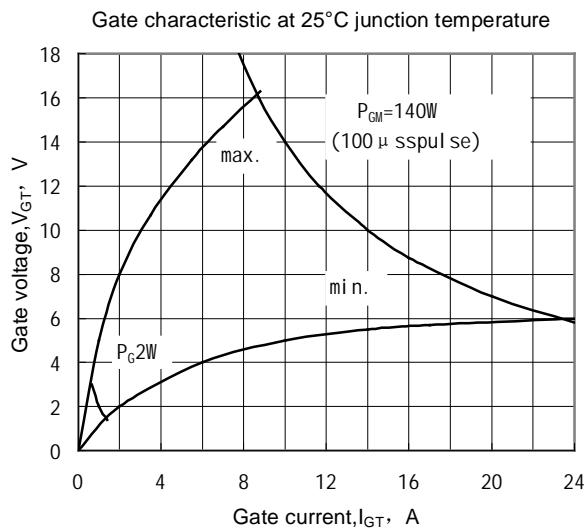


Fig.9

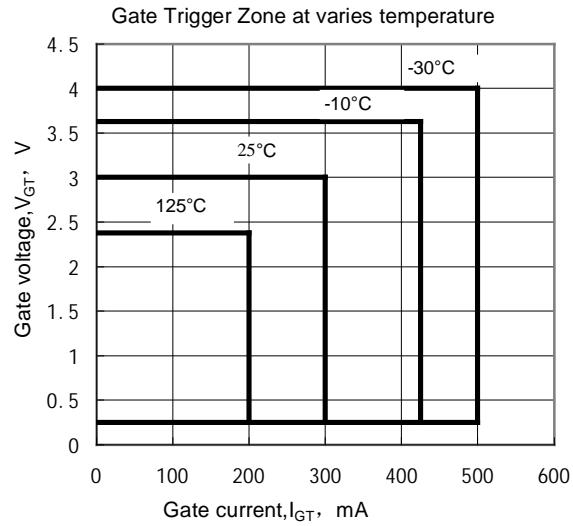


Fig.10

Outline: